



PATENT
Atty. Docket No. 408204
Express Mail Label No. EV206746462US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| Applicant(s) Efren M. Lacap et al. | Examiner Jonathan J. Johnson |
| Serial No. 10/648,586 | Group Art No. 1725 |
| Filed 26 August 2003 | Confirmation No. 4089 |
| For WAFER-LEVEL CHIP SCALE PACKAGE | |

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE

Sir:

These remarks are responsive to the Office Action of 23 June 2005. Please amend the above-identified application as indicated herein:

Written Request Pursuant to 37 C.F.R. §1.136(a)(3):

Please accept this authorization to treat any concurrent or future reply that requires a petition for extension of time for its timely submission as though the reply incorporates a petition for extension of time for the appropriate length of time.

An amendment to the Specification is on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims that begins on page 3 of this paper.

Amendments to the Drawings begin on page 6 of this paper and include an attached sheet replacing the sheet containing FIG. 11.

Remarks/Arguments begin on page 7 of this paper.